

Sub 2 2. (Amended) The semiconductor integrated circuit according to claim 1, wherein said timing changing circuit changes an inactivation timing of the reset signal in accordance with a first set of signals.

Cont A 3. (Amended) The semiconductor integrated circuit according to claim 1, further comprising a voltage generator for generating an internal supply voltage in accordance with an external supply voltage, and wherein said timing changing circuit changes an inactivation timing of the reset signal corresponding to said internal supply voltage.

Please add the following new claims:

Sub B -- 7. (New) A semiconductor integrated circuit comprising:

a power-on resetting circuit having a transistor, said power-on resetting circuit configured to inactivate a reset signal which initializes an internal circuit after activating the reset signal for a predetermined period, in response to a power supply being switched on, by utilizing a threshold voltage of the transistor, and to change the time that the reset signal is inactivated in accordance to a first adjusting signal;

a voltage generator having a transistor, said voltage generator configured to generating an internal supply voltage in accordance with an external supply voltage by utilizing a threshold voltage of the transistor, and able to vary the level of the internal supply voltage in accordance to a second adjusting signal; and

a timing changing circuit having, said timing changing circuit comprising:
a programming circuit configured to store logic values of said first adjusting signal and said second adjusting signal; and